



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al.

Art Unit : 2811

Serial No. : 10/623,581

Examiner : Stanetta Isaac

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Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

Amendments to the Claims begin at page 2 of this paper.

Arguments/Remarks begin at page 10 of this paper.